

ABSTRACT

A demodulation method for establishing clock synchronization within a short period of time from a received signal modulated by $\pi/4$ -shift QPSK modulation that contains a synchronization establishment signal wherein the change in phase periodically alternates between positive and negative, and demodulating said received signal includes a step of establishing synchronization from said received signal based on the timing of changes in the positive/negative polarity of the change in phase of the synchronization establishment signal contained in the received signal, and demodulating said received signal.

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